



# WR10X ±1%, ±5% General purpose chip resistors Size 1210

Customer	:
Approval No	•
Issue Date	:

Customer Approval :



## FEATURE

- 1. High reliability and stability
- 2. Reduced size of final equipment
- 3. Lower assembly costs
- 4. Higher component and equipment reliability
- 5. Lead free products upon customer requested

# APPLICATION

- Consumer electrical equipment
- Automotive application
- EDP, Computer application
- Telecom application

## DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to nominated value within tolerance which controlled by laser trimming of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

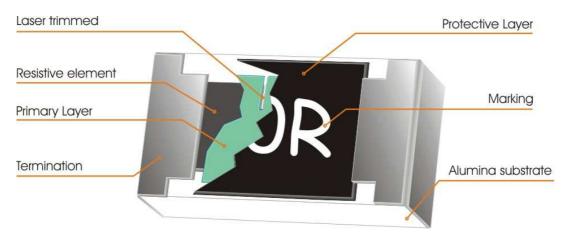


Fig 1. Consctruction of Chip-R

# QUICK REFERENCE DATA

Item	General Specification
Series No.	WR10X
Size code	1210 ( 3225 )
Resistance Tolerance	±1% ( E96/E24 ), ±5% ( E24 )
Resistance Range	Jumper, $1\Omega \sim 10M\Omega$ (E96+E24 series)
TCR (ppm/°C)	10R ~ 1M0, ≤ ± 100 ppm/°C
-55°C ~ +155°C	< 10R; > 1M0, $\le \pm 200 \text{ ppm/}^{\circ}\text{C}$
Max. dissipation at T <sub>amb</sub> =70°C	1/3 W ( 0.33 W )
Max. Operation Voltage (DC or RMS)	200V ( I = 2.6 Amp. for jumper )
Climatic category 55/155/56	

Туре	WR10X		
Power Rating At 70C	1/3 W		
Resistance	Max. 50mR		
Rated Current	2.5 A		
Peak Current	6 A		
Operating Temperature	-55C ~ 155C		

Note :

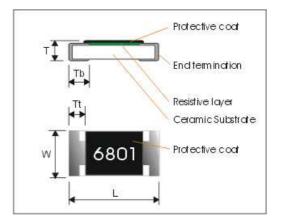
- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

 $RCWV = \sqrt{Rated Power \times Resistance Value}$  or Max. RCWV listed above, whichever is lower.

#### Dimensions:

Part No	WR10X
L	3.10 ± 0.10
w	$2.60 \pm 0.10$
Tt	$0.50 \pm 0.20$
Tb	0.50 ± 0.20 *1
t	0.55 ± 0.10

\*1 original 0.45+/-0.20





## Marking

#### 3-digits marking (±5%)

Each resistor is marked with a three digits code on the protective coating to designate the nominal resistance value. For values up to 9.1 the R is used as a decimal point. For values of 10.0 or greater the first 2 digits apply to the resistance value and third indicate the number of zeros to follow.

#### 4-digits marking (±1%)

Each resistor is marked with a four digits code on the protective coating to designate the nominal resistance value. For values of  $<97.6\Omega$  the R is used as a decimal point. For values of  $100\Omega$  or greater the first 3 digits are significant, the fourth indicates the number of zeros to follow.

#### Example

RESISTANCE	90Ω	100Ω	6800Ω	47000Ω
4-digits marking	90R0	1000	6801	4702
3-digits marking	-	101	682	473

## FUNCTIONAL DESCRIPTION

#### **Product characterization**

Standard values of nominal resistance are taken

from the E96 & E24 series for resistors with a tolerance of  $\pm$ 1%,  $\pm$ 5%. The values of the E24/E96 series are in accordance with "IEC publication 60063".

#### Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

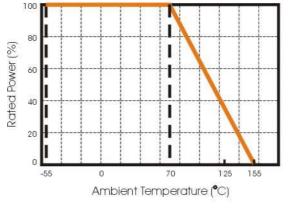


Figure 2 Maximum dissipation in percentage of rated power as a function of the ambient temperature

#### MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.



#### Walsin Technology Corporation

#### **SOLDERING CONDITION**

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs). Surface Mount Resistors are tested for solderability at 245°C during 3 seconds. The test condition for

Surface Mount Resistors are tested for solderability at 245°C during 3 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

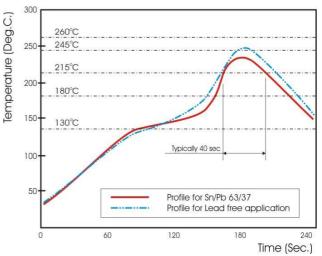


Fig 3. Infrared soldering profile for Chip Resistors

# CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

WR10	X	4702	J	т	L
Size code	Type code	Resistance code	Tolerance	Packaging code	Termination code
WR10: 1210	X : Thick	E96 +E24:	J: ± 5%	T: 7" Reeled taping	L = Sn base (lead free)
	film, 10R ~ 1M	3 significant digits followed by no. of zeros	zeros		iiee)
	W: < 10R ;	102Ω =1020	P: Jumper		
	> 1M0	37.4KΩ =3742			
		220Ω =2200			

Reeled tape packaging : 8mm width paper taping 5000pcs per 7" reel.



# TEST AND REQUIREMENTS(JIS C 5201-1: 1998)

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 68-1, subclause 5.3, unless otherwise specified.

Temperature: 15°C to 35°C.

Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar).

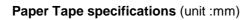
TEST	PROCEDURE / TEST METHOD	REQUIREMENT			
1631	PROCEDORE / TEST METHOD	Resistor	0Ω		
DC resistance	DC resistance values measured at the test voltages specified below :				
Clause 4.5	<10Ω@0.1V, <100Ω@0.3V, <1KΩ@1.0V,	Within the specified tolerance	$<50 m\Omega$		
	<10KΩ@3V, <100KΩ@10V, <1MΩ@25V, <10MΩ@30V				
Temperature	Natural resistance change per change in degree centigrade.	Refer to			
Coefficient of Resistance(T.C.R) Clause 4.8	$\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)}  t_1 : 20\% + 5\% - 1\%$	"QUICK REFERENCE DATA"	N/a		
	R1 : Resistance at reference temperature				
	R <sub>2</sub> : Resistance at test temperature				
Short time overload (S.T.O.L)	Permanent resistance change after a 5second application of a voltage 2.5 times RCWV or the maximum overload voltage specified in the above list, whichever is less.	ΔR/R max. ±(2%+0.10Ω)	<50mΩ		
Clause 4.13 Resistance to					
soldering heat(R.S.H) IEC 60068-2-58:2004	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 255 $^\circ\!C\pm\!5^\circ\!C$	$\Delta$ R/R max. ±(1%+0.05 $\Omega$ ) no visible damage	<50mΩ		
Solderability	Un-mounted chips completely immersed for 3±0.3 second in a SAC	95% coverage min., good tinning and no visible damage			
IEC 60068-2-58:2004	solder bath at 245℃ ±5℃				
Temperature cycling	30 minutes at -55°C±3°C, 2∼3 minutes at 20℃+5℃-1℃, 30 minutes at	AP/P = +(1% + 0.050)	< 50mΩ		
Clause 4.19	+155°C±3°C, 2~3 minutes at 20°C+5°C-1°C, total 5 continuous cycles	$\Delta$ R/R max. ±(1%+0.05 $\Omega$ )	< 201175		
Damp Heat	1000 +48/-0 hours, loaded with RCWV or Vmax in humidity chamber	10Ω≤R<1MΩ :			
(Load life in humidity)	controller at 40°C±2°C and 90~95% relative humidity, 1.5hours on and	$\Delta$ R/R max. ±(3%+0.10 $\Omega$ )	< 50mΩ		
Clause 4.24 0.5 hours off		R<10Ω, R≥1MΩ : ΔR/R max. ±(5%+0.10Ω)			
Load Life	1000+48/-0 hours; loaded with RCWV or $V_{\text{max}}$ in chamber controller				
(Endurance)	70±2°C, 1.5 hours on and 0.5 hours off	Ditto.			
Clause 4.25			ſ		
Bending strength	Resistors mounted on a 90mm glass epoxy resin PCB(FR4), bending	No visual damaged,	< 50mΩ		
Clause 4.33	once 3mm for 10sec.	$\Delta$ R/R max. ±(1%+0.05 $\Omega$ )	< JUII122		
Adhesion	Pressurizing force: 5N, Test time: 10±1sec.	No remarkable damage or remo	oval of the		
Clause 4.32		terminations			

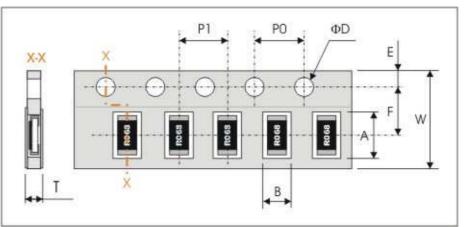


TEST	PROCEDURE / TEST METHOD	REQUIREMENT			
TEST	PROCEDURE/TEST METHOD	Resistor	0Ω		
Insulation	Apply the maximum overload voltage (DC) for 1 minute	R≧10GΩ			
Resistance					
JISC5201-1:1998					
Clause 4.6					
Dielectric Withstand	Apply the maximum overload voltage (AC) for 1 minute	No breakdown or flashover			
Voltage					
JISC5201-1:1998					
Clause 4.7					



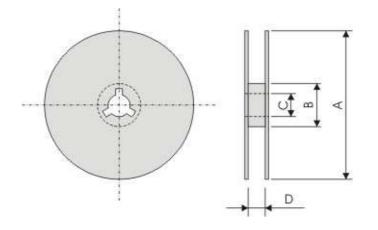
# PACKAGING





Component Size / Series	W		F	E	-	P0		ΦD
WR10X	8.00±0.30	3	5.50±0.20	1.75	±0.10	4.00±0.1	0	$\Phi 1.50^{+0.1}_{-0.0}$
Component Size / Series	А	A B		A B P1			Т	
WR10X	3.60±0.20	3.60±0.20 3.00±0.		.20	4.0	0±0.10		Max. 1.0

### **Reel dimensions**



Symbol	А	В	С	D
(unit : mm)	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5

## **Taping quantity**

- Chip resistors 5,000 pcs/reel